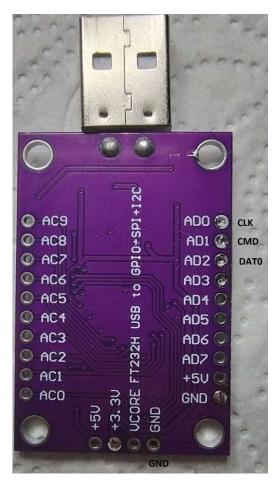
Connect the ft232h to the corresponding EMMC points (DO, CMD, CLK, GND) in circuit!!! Use cables as short as possible for cmd, clk, d0 (4cm works in my tests) Also connect the cables after the resistors or caps to emmc. emmc=>cmd point=>resistor=>grab here the signal

Short the crystal of your device to prevent the soc from booting.

Theoretically, it's sufficient to short the emmc at startup (cmd, clk, d0). This causes the SOC to enter boot mode and prevent access to the emmc. This worked with a Fire TV. No guarantee that it will work with other devices!



Open the folder with the program. Right-click on the executable and run as admin.



Click on Settings, a window will appear.

	Настройка	2	3
Config Load Success.	Mediatek EEPROM My Page Other Utilit Buffer Size -by Chip Protect Slice 93C46 T C 93CXX (reserved) C 34 Wir	Unblk Connection PostalAVR MaxSpeed Static	Save Setting Witer Read Wite
Info Clear	oMMC		Verify
Extra Control Reset Set ISP Status		ОК Отмена	Other

There should be such settings. Select Jtag/SPI, then Emmc. Set Max Speed.

Click the Open button. A window will appear. In it, check the boxes Force eMMC Set, MultiSector RW, Debug Show and Full Size RW

eMMC – 🗆 🗙
4 MB ✓ User Max Backup misc Re-Read All File Load CID ✓ Full Size RW misc Re-Read All File Load
FF 00 00 00 00 00 00 00 00 00 00 00 00 0
PARTITION_CONFIG[179] 00 INFO Write
BOOT_BUS_WIDTH[177] 00 INFO
RST_n_FUNCTION[162] 00 INFO
□ Byte-mode eMMC □ Debug Show Clear Log □ Force eMMC set □ MultiSector RW □ CardReader (Full Backup User Partition) Check Physical Drive (USB): □ □ INIT
Backup FileName: big_user.bin Write
Write Image: drop or open image Verify
Close

Click Close, then OK. Then Set ISP. We get a window. You should see something similar in the output

-ext_csd.bin saved . -Size = 3728MB -ext_csd_rev[192] = 0x07	^	eMMC	
- Rev 1.7 (MMC v5.0) - erased state =00 - reset_n enabled .		· Mar and a start of the	Save
Boot(1,2) size = 4194304 - boot1 boot enabled . - boot ack sent . - boot SDR mode .			Settings
reset bus after boot . ×8 bus for boot . usb-device closed.	ш		Read
	-	Source File (for flashing)	Write
Info Clear	ear	Selected file will be written to chip by press Write open	Verify
		Last Backup File (Name Auto) Buffer Size Auto Size	Auto
		boot1.bin, boot2.bin, user.bin Reset Auto Size	Exit

click read and wait until it's dumped completely

